

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
17 February 2005 (17.02.2005)

PCT

(10) International Publication Number
WO 2005/015740 A1

(51) International Patent Classification⁷: **H03K 5/08**

[GB/GB]; Cambridge Research Laboratory of Epson, 0a
Cambridge Science Park, Milton Road, Cambridge CB4
0FE (GB).

(21) International Application Number:
PCT/GB2004/003375

(74) Agent: **CLOUGHLEY, Peter, Andrew**; Miller Sturt
Kenyon, 9 John Street, London WC1N 2ES (GB).

(22) International Filing Date: 4 August 2004 (04.08.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0318238.3 4 August 2003 (04.08.2003) GB

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,
MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,
PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,
TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM,
ZW.

(71) Applicant (for all designated States except US): **SEIKO
EPSON CORPORATION** [JP/JP]; 4-1, Nishishinjuku
2-chome, Shinjuku-ku, Tokyo 163-0811 (JP).

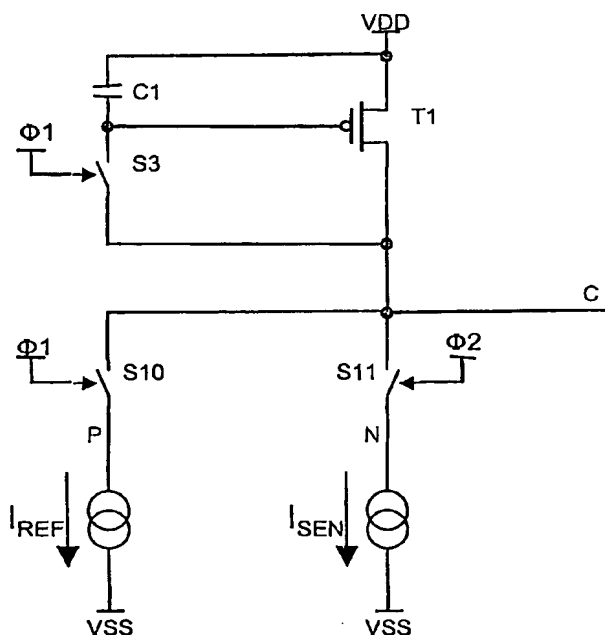
(72) Inventor; and

(75) Inventor/Applicant (for US only): **TAM, Simon**

(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,

[Continued on next page]

(54) Title: DIFFERENTIAL CIRCUITS



(57) Abstract: A single transistor current-mirror is interfaced with a suitable load in combination with a suitable set of switches to accomplish a comparator function. In particular, the differential circuit comprises a single transistor current mirror, including a capacitor connected to the transistor by a switch, and two current sources connected to the current mirror by respective and independent switches, the switch of one of the current sources being operated together with the capacitor and the switch of the other current source being operated so that the circuit operates as a source-follower amplifier with a current-source load. The comparator function is, thus, not influenced by the spatial distribution of the characteristics of the transistors used to implement the circuit.



ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,
SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

— before the expiration of the time limit for amending the
claims and to be republished in the event of receipt of
amendments

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ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.*